

Abstract of the Disclosure

A read circuit in a semiconductor device includes a circuit for determining whether data that have been read from a plurality of circuit blocks are to be supplied as output to the outside, and a discharging circuit that is inserted in cascade connection in data lines that are shared with a succeeding circuit blocks. If the output data that are to be transferred are at a low level, the discharging circuit is set to a conductive state and the data line that is shared with the succeeding circuit block is discharged, and if at a high level, the shared data line is precharged by a precharging circuit. This control operation is successively repeated as far as the lowest-order circuit block to supply, as output, data of a logic level that corresponds to the output data.